

APR 01 2004

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Chambers, et al. Docket No: TI-30883  
Serial No: 09/902,051 Examiner: Davidson, Dan  
Filed: 7/10/2001 Art Unit: 2651  
For: CMOS DC OFFSET CORRECTION CIRCUIT WITH PROGRAMMABLE HIGH-PASS TRANSFER FUNCTION

#11  
sm

**NOTICE OF APPEAL FROM THE PRIMARY EXAMINER  
TO THE BOARD OF APPEALS**

Assistant Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

<p>CERTIFICATION OF FACSIMILE TRANSMISSION I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9306 on the date shown below:</p> <p><i>Tommie Chambers</i> Tommie Chambers 4-1-04</p>
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Applicant hereby appeals to the Board of Appeals from the decision mailed 09/09/03, of the Primary Examiner finally rejecting Claims 1-10.

The item(s) checked below are appropriate:

- ☒ An extension of time to respond to the final rejection  
\_\_\_\_\_ was granted on \_\_\_\_\_.  
☒ Is requested for 1 month.
- ☐ A timely response to the final rejection has been filed, as provided in 841 O.G. 1411.
- ☒ Fee \$330.00:  
\_\_\_\_\_ Not required (Fee paid in prior appeal)  
☒ The Commissioner of Patents is hereby authorized to charge any fees which may be required or credit any overpayment to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.  
**This form is submitted in duplicate.**

Texas Instruments Incorporated  
P.O. Box 655474, MS 3999  
Dallas, TX 75265  
(972) 917-5633

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### FACSIMILE COVER SHEET

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Tommie Chambers April 1, 2004  
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NAME OF INVENTOR(S): Chambers	
RECEIPT DATE & SERIAL NO.: Serial No.: 09/902,051 Filing Date: 7/10/2001	
TITLE OF INVENTION: CMOS DC OFFSET CORRECTION CIRCUIT WITH PROGRAMMABLE HIGH-PASS TRANSFER FUNCTION	
TI FILE NO.: TI-30883	DEPOSIT ACCT. NO.: 20-0668
FAXED: 04/01/2004 DUE: 03/08/2004 ATTY/SEC'Y: WDS:tlc	

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